



Attorney Docket No. 0756-7254

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Kiyoshi KATO et al.

Serial No. 10/775,328

Filed: February 11, 2004

For: SEMICONDUCTOR DEVICE

) Group Art Unit: 2891

) Examiner: S. Fulk

) CERTIFICATE OF MAILING

) I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Service in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on December 27, 2005.



INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing information known to Applicant and requests that this information be made of record in the above identified application. Copies are submitted herewith in accordance with 37 C.F.R. 1.98(a).

Copies of U.S. patents and U.S. publications are not enclosed in accordance with the Notice published in the Official Gazette on August 5, 2003 entitled *Information Disclosure Statements May Be Filed Without Copies of U.S. Patents and Published Applications in Patent Applications filed after June 30, 2003*, which waives the requirement under 37 CFR 1.98(a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. publication.

The Applicants direct the Examiner's attention to U.S. Patent Application Publication No. 2004/007706 to Yamazaki et al., which was submitted in the Information Disclosure Statement filed June 14, 2004.

Please note, JP 3397565 resulted from JP 8-47561 application. JP 09-246509 is also based on the JP '561 application. The abstract for JP '509 is attached to JP '565 to facilitate the consideration of JP '565.

U.S. Patent No. 5,483,263 is in the family of EP 0 633 542.

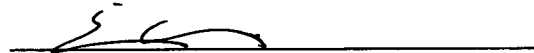
WO 01/33487 is in the family of DE 199 63 165.

U.S. Patent Publication No. 2003/0139520 and EP 1 281 690 are in the family of WO 01-64598.

A check in the amount of \$180 is being submitted to comply with the provisions of 37 C.F.R. § 1.97(c).

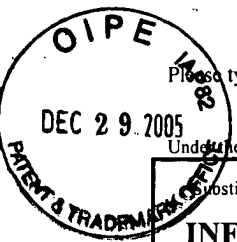
The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 50-2280. A duplicate copy of this sheet is attached.

Respectfully submitted,



Eric J. Robinson
Reg. No. 38,285

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known			
				Application Number		10/775,328	
				Filing Date		February 11, 2004	
				First Named Inventor		Kiyoshi KATO et al.	
				Group Art Unit		2891	
				Examiner Name		S. Fulk	
Sheet	1	Of	2	Attorney Docket Number		0756-7254	

U.S. PATENT DOCUMENTS						
Examiner Initials [*]	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ³ (if known)			
		2003/0122137		Hashimoto	07/03/2003	
		2003/0139520		Toyama et al.	07/24/2003	
		2003/0218174		Verdonk et al.	11/27/2003	
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		Office ³	Number ⁴	Kind Code ⁵ (if known)				
		DE	199 63 165			03/08/2001		Abst.
		JP	05-037357			02/12/1993		Abst.
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		EP	1 281 690			02/05/2003		Eng.
		WO	01/33487			05/10/2001		Abst.
		WO	01/64598			09/07/2001		Abst.
		WO	02/31882			04/18/2002		Abst.

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS					
Examiner Initials [*]	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T ²
		Partial European Search Report dated April 8, 2004 for Application No. 03009851.1.			
		J.A. ROGERS et al., <i>Organic Smart Pixels and Complementary Inverter Circuits Formed on Plastic Substrates by Casting and Rubber Stamping</i> , IEEE Electron Devices Letters, Volume 21, No. 3, March 2000, Pages 100-103.			
Examiner Signature				Date Considered	

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known		
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Sheet		2	of	2	Filing Date	February 11, 2004
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		European Search Report dated July 1, 2004 for Application No. 03009851.1.	
		C. WU et al., <i>P-19: Design of a Novel a-Si PIN/OLED Image Sensor & Display Device</i> , SID Digest '99, SID International Symposium Digest of Technical Papers, 1999, Pages 528-531.	
Examiner Signature			Date Considered

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